

# High C/C++\* Embedded Development Toolset

- Support for Load Scheduling
- "Flat" 386 Memory Model Libraries Included
- Math Functions Inlined Directly in the Code
- Nine Levels of Global Optimization
- Extensive Error/Warning Messages
- Native and Emulated Floating-Point Code Generation Available
- Source-Annotated Assembly Listings
- Inline Functions Substituted Across Compilation Units
- Place Code in Specific Locations
- Adjustable External Naming Conventions

The High C/C++\* Embedded Development Toolset provides proven technology for creating robust embedded systems applications for Intel's family of 32-bit embedded processors.

The toolset includes a C/C++ compiler and ANSI-compliant libraries. Optionally, a linker/locator, remote target debugger, and simulator are available through Systems and Software Incorporated (SSI).

High C/C++ tools produce machine language that is compact, fast, and efficient, allowing complex applications to be developed and deployed.

MetaWare's development strategy is to provide compatibility between the High C/C++ Toolset and the leading real-time operating systems, debuggers, and emulators. This open systems approach to embedded application development will shorten time-to-market cycles as well as development time.

Advanced optimization techniques include common sub-expression elimination, loop invariant analysis, function inlining, loop unrolling, live/dead analysis, tail merging, register lifetime analysis, and load/store scheduling. Code may be optimized for space or for execution speed.



Constants, literals, and read-only scripts may be placed into code instead of data. The compiler properly handles "erno" when inlining functions from math.h. You may also choose to put initialization information for automatic aggregates into a separate code section instead of placing it in the data section.

High C/C++ supports a BSS section, as well as the ability to override any segment, class, or group name. Also supported are packed and unpacked structures plus the ability to change member alignment.

HOST SYSTEMS SUPPORTED:  
Pentium®, i486™, and i386™ processors, running DOS, Windows\* 3.1, Windows 95, or Windows NT

PROCESSORS SUPPORTED:  
Intel386 CX/SX/EX/SXSA/DX, Intel486 SX, IntelDX2™, IntelDX4™, and Pentium processors

AVAILABILITY:  
Now

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